**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**

(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
02Micro 03.15

In Re Application Of: SPENEA et al

Serial No.
10/624,295Filing Date
July 22, 2003

Examiner

Group Art Unit
2816Title: **CIRCUIT AND METHOD FOR TRIMMING LOCKING OF INTEGRATED CIRCUITS**Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

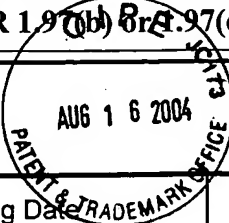
2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);**OR**☐ the fee set forth in 37 CFR 1.17(p).

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CIRCUIT AND METHOD FOR TRIMMING LOCKING OF INTEGRATED CIRCUITS

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **50-2121** as described below.
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Signature

Dated: August 12, 2004

Edmund P. Pfleger

Reg. No. 41,252

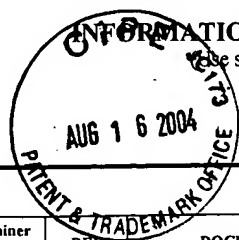
Grossman, Tucker, Perreault & Pfleger, PLLC

55 South Commercial Street

Manchester, New Hampshire 03101

603-668-6560

CC:

**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

Docket Number (optional)

O2Micro 03.15

Application No.

10/624,295

Applicants(s)

Spenea et al.

Filing Date

July 22, 2003

Group Art Unit

2816

U.S. PATENT DOCUMENTS

*Examiner Initial	REF	DOCUMENT NUMBER	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
		5,079,516	01/07/92	Russell et al.	330/261	
		6,338,032	01/08/02	Chen	703/16	
		6,472,897	10/29/02	Shyr et al.	324/765	
		6,410,398	6/25/2002	Forel et al	438/382	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		S. Laville, S. Pontarollo, "Integrated Offset Trimming Technique", ST Microelectronics DSG, and D. Dufaza, D. Auvergne, LIRMM (date unknown).

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include Copy of this form with next communication to applicant